

FIG. 14

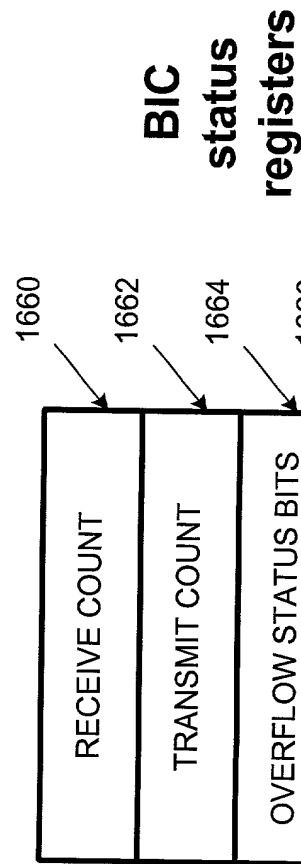
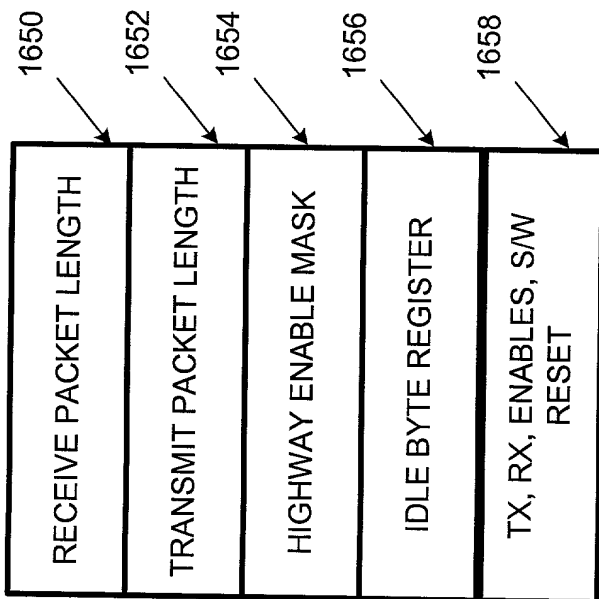


FIG. 16B

IX Bus Quadword

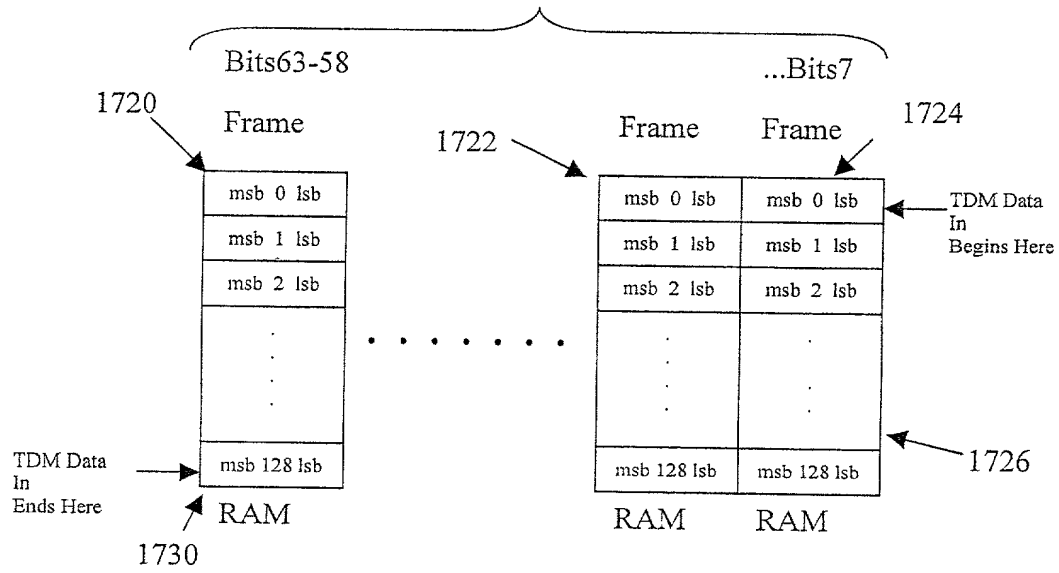


FIG. 17

RAM8	RAM7		RAM6		RAM5		RAM4		RAM3		RAM2		RAM1		RAM0	
Spare RAM Bank	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e
	7	7	6	6	5	5	4	4	3	3	2	2	1	1	0	0
	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0	H W Y 1	H W Y 0

FIG. 18